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SERIAL NUMBER 10/731,072	FILING DATE 12/09/2003  RULE	CLASS 324	GROUP ART UNIT 2829	ATTORNEY DOCKET NO. ROC920030297US1
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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

None at

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

None at

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY VT	SHEETS DRAWING 4	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged Examiner's Signature			

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## TITLE

Method and testing circuit for tracking transistor stress degradation

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